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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10067151	01/29/2002	716	16	2825	DO

**APPLICANTS: Rupp Charles; Garverick Timothy; Arnold Jeffrey;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/265,303 01/30/2001

YES TO

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☒

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no

35 USC 119 conditions met ☐ yes ☒ no

Verified and Acknowledged Examiners's initials TD

ATTORNEY DOCKET NO

STRE-003/01US

TITLE : Design methodology for merging programmable logic into a custom IC

U.S. DEPT. OF COM. / PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for 0.0	
		DRAWING	
		Sheets Drawn	Fig. Drawn
			Print Fig.
<input checked="" type="checkbox"/> TERMINAL		Primary Examiner	
DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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